



## VIVEKANANDA COLLEGE OF ENGINEERING AND TECHNOLOGY, PUTTUR

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

# SPARKLE

A mirror of ECE

Volume 3

November 3, 2016

- Student Association Inauguration
- A workshop was conducted on Simulation of Digital Modulation Techniques
- Workshop on Development of Android Mobile Applications
- workshop on Power electronics circuit simulation using OrCad Pspice
- workshop on Simulation of Basic Electronics Circuit Using OrCad
- workshop on simulation of Analog Electronic Circuits using OrCAD tool
- One day Workshop on Simulation of Analog circuits using Orcad
- A national level Five Day Workshop on Board Design
- One day workshop on Power Electronic Circuit simulation using PSPICE
- Six Day Workshop on Embedded Systems Design
- Faculty Recharge Program
- A technical talk on Air-bag System Overview
- Three Day Faculty Development Program on Sci lab
- Achievements
- A technical seminar on Telecom Standards

### STUDENT ASSOCIATION INAUGURATION

Inauguration of Association of Electronics and Communication Engineering (AECE) was held on 19/09/2016. It was inaugurated by Mr. Yashaswi Narayan, Alumni, Senior Engineer in L&T, Mysore. Shri. Radhakrishna Bhaktha P, Correspondent of Vivekananda College of Engineering & Technology, Puttur, graced the occasion. Presidential Address was given by Principal Dr. M S Govinde Gowda.



### WORKSHOP ON SIMULATION OF DIGITAL MODULATION TECHNIQUES USING SIMULINK

A workshop was conducted on Simulation of Digital Modulation Techniques using Simulink on 20-08-2016 by Mr.Gurusandesh M and Mr.Mahabaleshwara Bhat P, Department of Electronics and Communication for the third year students of Electronics and Communication

### WORKSHOP ON DEVELOPMENT OF ANDROID MOBILE APPLICATIONS



A two days workshop on Development of Android Mobile Applications was conducted from 24-09-2016 to 25-09-2016 by Mr.Venugopal, Associate Professor, Dept of Computer Science, and Ms.Ashwini Venugopal, Associate Professor, Dept of Information Science, NMAMIT, Nitte for the final year students of Electronics and Communication.

### POWER ELECTRONICS CIRCUIT SIMULATION USING ORCAD PSPICE

A workshop was conducted on Power Electronics Circuit Simulation using OrCad Pspice on 20-08-2016 by Ms.Sowmya Anil and Ms.Jovita Lasrado, Department of Electronics and Communication for the final year students of Electronics and Communication.

### WORKSHOP ON SIMULATION OF BASIC ELECTRONICS CIRCUIT USING ORCAD



A workshop was conducted on Simulation of Basic Electronics Circuit Using Orcad from 22-08-2016 to 23-08-2016 by Mr.Suhandas, Mr.Vinay P, Ms.Sowmya Anil, Mr.Mahabaleshwara Bhat, Ms.Nirupama and Mr.Shreyas H, Department of Electronics and Communication for II PUC students of Vivekananda PU College, Puttur.

### WORKSHOP ON SIMULATION OF ANALOG ELECTRONIC CIRCUITS USING ORCAD TOOL

One day workshop on simulation of Analog Electronic Circuits using OrCAD tool was conducted on 20-08-2016 by Mr.Suhandas and Ms.Nirupama K, Department of Electronics and Communication for the second year students of Electronics and Communication.

### ONE DAY WORKSHOP ON SIMULATION OF ANALOG CIRCUITS USING ORCAD

A one day Workshop on Simulation of Analog circuits using Orcad was conducted on 08-10-2016 by Mr.Vinay P. and Ms. Nandini Rao G., Department Electronics and Communication for the second year students of Electronics and Communication.

"That knowledge which purifies the mind and heart alone is true Knowledge, all else is only a negation of Knowledge."

— Shri Ramakrishna Paramahansa





## A NATIONAL LEVEL FIVE DAY WORKSHOP ON BOARD DESIGN



A national level Five Day Workshop on Board Design was conducted on 19-07-2016 to 23-07-2016 by Mr. S M Narasimhan, Freelance Consultant, Mysore. 20 external participants from neighboring colleges and 30 faculty members from Vivekananda College of Engineering and Technology participated. Participants were able to understand schematic design entry basics, PCB design basics, Analog system design and Gerber plots verification and gained more knowledge on PCB design as well as embedded system.

## WORKSHOP ON POWER ELECTRONIC CIRCUIT SIMULATION USING PSPICE

One day workshop on Power Electronic Circuit simulation using PSPICE was conducted on 06-08-2016 by Mr. Suhandas and Mr. Vinay P., Department of Electronics and Communication for the final year students of Electronics and Communication.

## WORKSHOP ON EMBEDDED SYSTEMS DESIGN

A Six Day Workshop on Embedded Systems Design was conducted from 18-07-2016 to 23-07-2016 by Mr. Mahesh Padyana, CEO of CloudE, Mysore for the third year students of Electronics and Communication.



## FACULTY RECHARGE PROGRAM

Faculty Recharge Program on Digital signal processing and Electromagnetic theory was conducted from 04-08-2016 to 06-08-2016 by Dr. Roshan Joy Martis and Mr. Udaya Kumar S, Department of Electronics and Communication for the faculty members of Department of Electronics and Communication.

## A TECHNICAL TALK ON AIRBAG SYSTEM OVERVIEW



A technical talk on Airbag System Overview was conducted on 13-08-2016 by Mr. Pavan Bhide, Senior Technical Specialist, Continental, Bangalore for the final year students of Electronics and Communication.



## THREE DAY FACULTY DEVELOPMENT PROGRAM ON SCI LAB

Three Day Faculty Development Program on Sci lab was conducted from 01-09-2016 to 03-09-2016 by Mr. Shivaprasad, Department of Electronics and Communication for the faculty members of Department of Electronics and Communication.

## ACHIEVEMENTS

The total citations to the research articles published by Dr. Roshan Joy Martis, have crossed 1,000 on 1<sup>st</sup> of November. His H-index is 19. He is a faculty member in the department of Electronics and Communication Engineering of Vivekananda College of Engineering and Technology, Puttur. Only few researchers of his age in the country and in the world have attained this distinction. Total citations of 1,000 means, authors of different articles/ research papers have referred the articles of a given researcher in 1,000 instances. Only 15% of researchers of all age in the world have this distinction of 1,000 citations.

Dr. Roshan, is a author of 55 research papers and his research interests are Signal Processing, Machine Learning, Pattern Recognition, Optimization etc. with applications in Biomedicine and communication engineering. He is serving as an Editorial Board Member in three International Journals. He is a Senior Member of Institute of Electrical and Electronics Engineers (IEEE), USA.



## A TECHNICAL SEMINAR ON TELECOM STANDARDS

A technical seminar on Telecom Standards was conducted on 24-09-2016 by Mr. Vishnu G Naik, Asst. General Manager, BSNL, Mangalore for the students of third year as well as for the faculty members of Department of Electronics and Communication & Computer Science Engineering.