

**ACADEMIC YEAR 2021-2022**

<b>SNo</b>	<b>Activities</b>	<b>Date-Month- Year</b>	<b>Number of Participants</b>	<b>Details of Participants</b>	<b>PO/PSO</b>
1	<b>Best project award in KSCST 2020-21</b>	<b>August 2021</b>	<b>4</b>	<b>Mr. Anantheshwar Pai B Mr. Anoop J Mr. Ashish Shetty Mr. Deekshith P S</b>	<b>PO8 PO9 PO10 PO11 PO12</b>



# **KSCST**

**KARNATAKA STATE COUNCIL  
FOR SCIENCE AND TECHNOLOGY**

The final year students of Department of Electronics and Communication Engineering won first prize in 44th series Student Project Programme (SPP) 2020-21 organized by KSCST. It was conducted through online mode. Mr. Anantheshwar Pai B, Mr. Anoop J, Mr. Ashish Shetty, Mr. Deekshith P S of final year ECE done their project on 'Smart Automated Irrigation with Mobile Application using IOT' under the guidance of Dr. Mahantesh R Choudhari has won first prize.

**ACADEMIC YEAR 2021-2022**

SNo	Activities	Date-Month- Year	Details of Participants	Details of Resource Person(s)/ Guest	PO/PSO
2	Alumni Webinar Series “Grooming on Interviews for Carrier Aspirants”	27-08-2021	40	Mrs. Kavyanjali H M Software Engineer GE Transportation Bangalore	PO8 PO9 PO10 PO11 PO12


**VIVEKANANDA COLLEGE OF ENGINEERING & TECHNOLOGY**  
 Nehru Nagar, Puttur - 574203


**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**Alumni Webinar Series**




**Grooming on interviews for Career Aspirants**



**Ms. Kavyanjali H M**  
 B.E., Electronics & Communication Engg.  
 Software Engineer  
**GE TRANSPORTATION, BANGALORE.**

 **27<sup>th</sup> August 2021**

 **04:00 pm**

Platform:  
 Microsoft Teams

The Department of Electronics and Communication Engineering, Vivekananda College of Engineering and Technology, Puttur, organized an Alumni Webinar Series on “**Grooming on Interviews for Carrier Aspirants**” on 27<sup>th</sup> August 2021 from 4:00-5:00pm. The session was conducted by alumnus **Mrs. Kavyanjali H M**, Software Engineer, GE Transportation, Bangalore. Mrs. Kavyanjali H M offered valuable information and tips about resume building, preparation for Interviews, the type of questions that could be expected during interviews. The session was conducted through Microsoft Teams. There were 40 participants from prefinal year of Electronics and Communication Engineering department of VCET.

**ACADEMIC YEAR 2021-2022**

SNo	Activities	Date-Month- Year	Details of Participants	Details of Resource Person(s)/ Guest	PO/PSO
3	Maventic Placement Preparations	07-10-2021	Final year students of ECE	Mr. Ajay Kumar Senior Software Developer, Maventic	PO1 PO6 PO8 PO9 PO10

**VIVEKANANDA COLLEGE OF ENGINEERING & TECHNOLOGY**  
Nehru Nagar, Puttur - 574203

**Campus Recruitment Drive**

**JOB**

**maventic**

**For CSE/ECE 2021-2022 batch**

**on Friday 08/10/2021**

**Salary Package 4.0LPA**

A pre-placement preparation for Maventic was conducted by Mr. Ajay Kumar, Senior Software Developer, Maventic with an objective to prepare the students for Maventic placement drive. Session was conducted for final year students of ECE Dept., on 7<sup>th</sup> October 2021 at 7.00PM, through Microsoft teams. A brief introduction was given on how to prepare for Maventic Placements and resource person briefed about the number of rounds and type of questions which will appear for the drive.

The event was organized by Dept. of ECE and Coordinated by Mr. Suhandas, Placement Coordinator, ECE Dept. VCET Puttur.

**ACADEMIC YEAR 2021-2022**

SNo	Activities	Date-Month- Year	Details of Participants	Details of Resource Person(s)/ Guest	PO/PSO
4	<b>Infosys Placement Preparations</b>	16-10-2021	Final year students of ECE	Mr. Mohammed Niyaz Team Lead, Infosys Mangalore	PO1 PO6 PO8 PO9 PO10

**VIVEKANANDA COLLEGE OF ENGINEERING & TECHNOLOGY**  
Nehru Nagar, Puttur - 574203

**Infosys**®  
Campus Recruitment Drive

**on Sunday  
03/10/2021**

**For  
All Branches  
2021-2022 batch**

**Job Position  
System Engineer**

**Salary Package  
3.60LPA**

A placement preparation for Infosys was conducted by Mr. Mohammed Niyaz, Team Lead, Infosys Mangalore with an objective to prepare the students for Infosys final Interview. Session was conducted for first round shortlisted students of Infosys across the branch (ECE, CSE, ME and CV) on 16<sup>th</sup> October 2021 at 10.00AM, through Microfost teams. Recourse person briefed and guided, about how the students should prepare for the final Infosys interview.

The event was organized by Dept. of ECE and Alumni Cell. Coordinated by Mr. Suhandas, Placement Coordinator, ECE Dept. VCET Puttur.

**ACADEMIC YEAR 2021-2022**

SNo	Activities	Date-Month- Year	Details of Participants	Details of Resource Person(s)/ Guest	PO/PSO
5	Sasken Placement Preparations	21-10-2021	Final year students of ECE	Mr. Rohit B L System Software Engineer Sasken	PO1 PO6 PO8 PO9 PO10

**VIVEKANANDA COLLEGE OF ENGINEERING & TECHNOLOGY**  
Nehru Nagar, Puttur - 574203

**Campus Recruitment Drive**

**SASKEN**  
**Sasken Technologies**

**For ECE**  
**2021-2022 batch**

**on Saturday**  
**23/10/2021**

**CTC Offered**  
**3.7LPA**

A pre-placement preparation for Sasken was conducted by Mr. Rohit B L, System Software Engineer, Sasken with an objective to prepare the students for Sasken placement drive. Session was conducted for final year students of ECE Dept., on 21<sup>st</sup> October 2021 at 7.00PM, through Microsoft teams. A brief introduction was given on how to prepare for Sasken Placements and resource person briefed about selection rounds and type of questions which would appear for the drive.

The event was organized by Dept. of ECE and Coordinated by Mrs. Sowmya Anil, Placement Coordinator, ECE Dept. VCET Puttur.

**ACADEMIC YEAR 2021-2022**

SNo	Activities	Date-Month- Year	Details of Participants	Details of Resource Person(s)/ Guest	PO/PSO
6	Webinar on “Embedded Hardware Design and Manufacturing Overview and Insights”	03-12-2021	40	Ms.Vinymol Thambi, Technical Manager at Velankani Electronics Pvt. Ltd.	PO1 PO2 PO3 PO5 PO12 PSO1



IEEE VCET student branch in association with Department of Electronics and Communication Engineering, VCET Puttur organized a webinar on “Embedded Hardware Design and Manufacturing Overview and Insights” on 3 December 2021. This Webinar was conducted by **Ms.Vinymol Thambi**, Technical Manager at Velankani Electronics Pvt. Ltd.

Ms.Vishrutha K S of 7th semester ECE welcomed the participants. Prof. Rajani Rai, Dept. of ECE Introduced the speaker. Ms. Vinymol Thambi explained all aspects of basic electronics and the areas of designing electronic devices. It gave a deep insight over the layout designing and PCB designing domain. The webinar ended with the question-and-answer session.

Mr. Gautam Shankar delivered the vote of thanks. Ms.Divyashree hosted the webinar.

**ACADEMIC YEAR 2021-2022**

<b>SNo</b>	<b>Activities</b>	<b>Date-Month- Year</b>	<b>Details of Participants</b>	<b>Details of Resource Person(s)/ Guest</b>	<b>PO/PSO</b>
7	An Industrial visit to the Bangalore International Exhibition center “Electronica India”	16-12-2021	56	Prof. Suhandas Assistant Professor, Department of ECE, VCET Puttur	PO1, PO2, PO3, PO5, PO9, PO12 PSO1

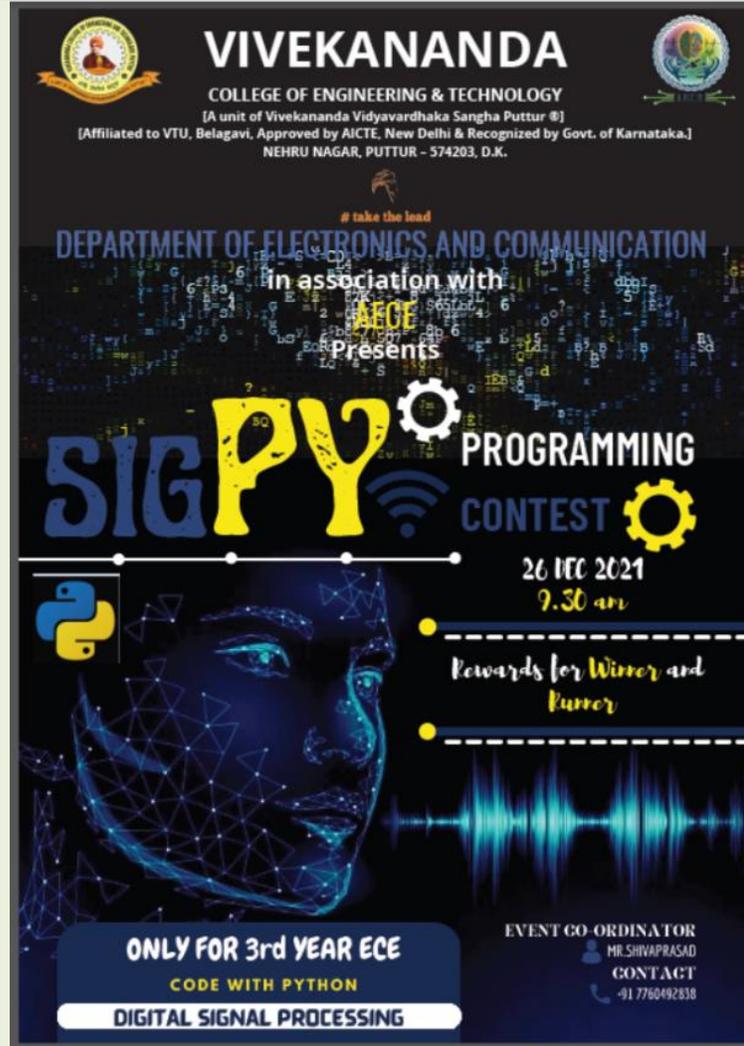


The Department of ECE has organized an Industrial visit to the Bangalore International Exhibition center at Bengaluru. It is an exhibition named “Electronica India”, an international trade fair for electronic components, systems, application, and solution.

There were 56 students from ECE visited exhibition with three faculties. Electronica gave information about latest technologies that are present in India in the field of electronics, exposure to how the corporate world is, and internship opportunities in well-developed companies.

**ACADEMIC YEAR 2021-2022**

SNo	Activities	Date-Month- Year	Details of Participants	Details of Resource Person(s)/ Guest	PO/PSO
8	“Programming competition using Python SIGPY”	26-12-2021	20	Prof. Shivaprasad Assistant Professor, Department of ECE, VCET Puttur	PO1 PO2 PO3 PO5 PO9 PO12



Department of Electronics and Communication Engineering in association with AECE organized programming competition using Python SIGPY on December 26<sup>th</sup>, 2021. 20 students of third year ECE participated in the event. Event winners were decided based on the maximum number questions solved with correct logic.

Event Winners: Ms. Divyashree and Mr. Praneeth B of 5<sup>th</sup> sem ECE secured 1<sup>st</sup> and 2<sup>nd</sup> prize respectively.

**ACADEMIC YEAR 2021-2022**

SNo	Activities	Date-Month- Year	Details of Participants	Details of Resource Person(s)/ Guest	PO/PSO
9	Technical competition ElecTechX organized by IEEE student branch	29-12-2021	80	IEEE student branch, Department of ECE VCET, Puttur	PO1 PO2 PO5 PO10 PO12



A one-day intra-college event ElecTechX was organized by IEEE student branch, Department of ECE, VCET, Puttur on 29/12/2021. 27 teams had registered and more than 80 students of second and third year ECE had participated in the events like “TranQUIZ”, “Circuitrix”, “One min Jam” and “Runtime Terror”.

## ACADEMIC YEAR 2021-2022

SNo	Activities	Date-Month- Year	Details of Participants	Details of Resource Person(s)/ Guest	PO/PSO
10	Virtual Alumni Meet	04-01-2022 05-01-2022 06-01-2022 07-01-2022 10-01-2022 11-01-2022	164	Alumni Association, VCET, Puttur (2001-2020)	-



Department of Electronics and Communication Engineering, Vivekananda College of Engineering and Technology, Puttur in coordination with Alumni Association, VCET organized a virtual alumni meet from 04-01-2022 to 11-01-2022. Around 164 alumni of VCET-ECE participated in this event and shared valuable inputs for the betterment of Department and institution. 6 sessions were conducted according to passed out years (2001-2020). Alumni interaction was good, and they shared memorable moments of their college life too. They also shared their views of professional life and student life. They insisted to do courses on Course era, UdeMy etc to gain knowledge on various domains which is very essential for the corporate field.

Prof. Shrikanth Rao S K, HoD, Department of ECE welcomed the gathering and gave brief introduction about the college facilities of present day. Prof. Nisha G R, Alumni cell coordinator, Department of ECE delivered vote of thanks and coordinated the event. Faculties of ECE department were also attended the virtual meet and interacted with the alumni.

**ACADEMIC YEAR 2021-2022**

SNo	Activities	Date-Month- Year	Details of Participants	Details of Resource Person(s)/ Guest	PO/PSO
11	Alumni Webinar Series “VLSI industry overview”	13-01-2022	50	Mr. K. Pramod Baliga, MTS Silicon Design Engineer, AMD (Advanced Micro Devices) Bangalore	PO1 PO6 PO8 PO9 PO10

**WEBINAR on**

# VLSI Industry Overview

**K. Pramod Baliga**  
 MTS Silicon Design Engineer  
 AMD (Advanced Micro Devices)  
 Alumnus E&C(2009-2013)

**January 13 thursday | 3.00PM**

Department of Electronics and Communication Engineering  
 in association with  
 Association of Electronics & Communication Engineering (AECE)

**VIVEKANANDA**  
 COLLEGE OF ENGINEERING & TECHNOLOGY  
 [A unit of Vivekananda Vidyavardhaka Sangha Puttur \*]  
 Nehru Nagar, Puttur - 574203

Platform: Microsoft Teams

The Department of Electronics and Communication Engineering, Vivekananda College of Engineering and Technology, Puttur, organized an Alumni Webinar Series on “**VLSI industry overview**”. The session was conducted by alumnus **Mr. K. Pramod Baliga**, MTS Silicon Design Engineer, AMD (Advanced Micro Devices) Bangalore.

Mr. K Pramod Baliga spoke about the Integrated Circuits, their applications and importance, also gave an insight on Semiconductor industry, Digital Design Flow and Chip Development. He further emphasised on the need of building a strong technical foundation and suggested the students to make use of the Internet for the same. He continued by solving some basic circuits which was an interesting part of the session.

The session was conducted through Microsoft Teams. There were 50 participants from pre final year of Electronics and Communication Engineering department of VCET.

**ACADEMIC YEAR 2021-2022**

SNo	Activities	Date-Month- Year	Details of Participants	Details of Resource Person(s)/ Guest	PO/PSO
12	Alumni Webinar Series “Career in VLSI and Embedded System Design”	22-01-2022	45	Mr. Dileep Kudva, Senior Verification Engineer, Dialog Semiconductor Renesas Company United States	PO9 PO10 PO12 PSO1 PSO2

Alumni Talk on

**Career in VLSI and Embedded System Design**

**Mr. Dileep Kudva**  
Senior Verification Engineer  
Dialog Semiconductor Renesas Company  
United States

January 22 Saturday | 9.30AM

organized by  
Department of  
Electronics and Communication Engineering  
in association with Alumni Cell  
and  
Association of Electronics & Communication Engineering (AECE)

**VIVEKANANDA**  
COLLEGE OF ENGINEERING & TECHNOLOGY  
[A unit of Vivekananda Vidyavardhaka Sangha Puttur \*]  
Nehru Nagar, Puttur - 574203

The Department of Electronics and Communication Engineering, Vivekananda College of Engineering and Technology, Puttur, organized an Alumni Webinar Series on “Career in VLSI and Embedded System Design”. The session was conducted by alumnus Mr. Dileep Kudva, Senior Verification Engineer, Dialog Semiconductor Renesas Company, United States. Mr. Dileep Kudva offered valuable information and tips. He discussed about Opportunities in semiconductor industries, upskilling oneself and Projects related to Electronics . The session was conducted through Microsoft Teams. There were 45 participants from final year of Electronics and Communication Engineering department of VCET.

**ACADEMIC YEAR 2021-2022**

<b>SNo</b>	<b>Activities</b>	<b>Date-Month- Year</b>	<b>Number of Participants</b>	<b>Details of Participants</b>	<b>PO/PSO</b>
<b>13</b>	<b>Final year Project selected for IRJET-2022</b>	<b>Jan 2022</b>	<b>4</b>	<b>Karthik Prasad Madhav raj B Gokulnath S SanjanGovin K</b>	<b>PO1 PO6 PO8 PO9 PO10</b>

A project paper of final year students Karthik Prasad, Madhav raj B, Gokulnath S and Sanjan Govind K has got selected in international journal IRJET with the title as “DEEP LEARNING ASSISTED TOOL FOR FACE MASK DETECTION”, Volume-9, issue-1, Jan 2022, e-ISSN 2395-0056, under the guidance of Mr. Shivaprasad.

**ACADEMIC YEAR 2021-2022**

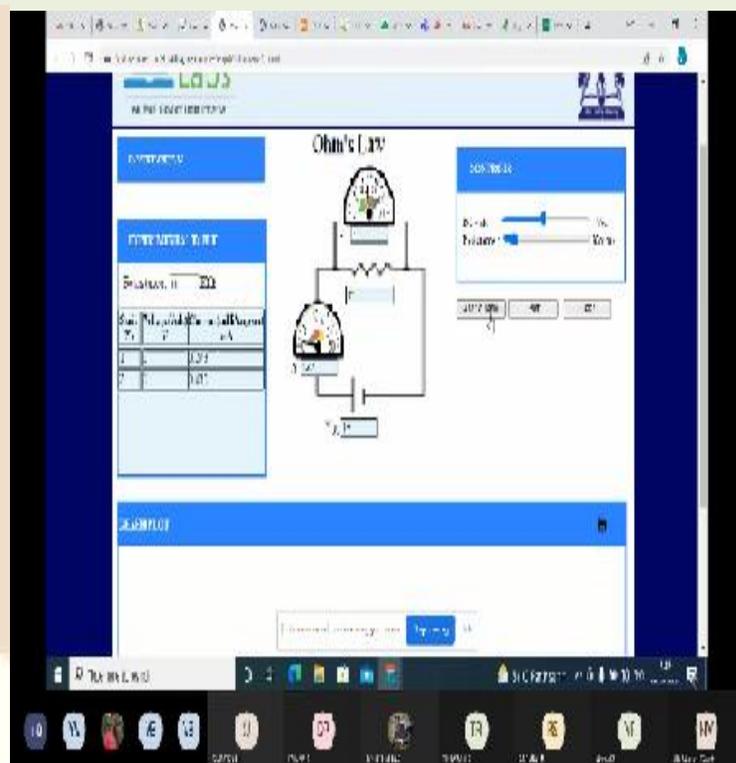
SNo	Activities	Date-Month- Year	Details of Participants	Details of Resource Person(s)/ Guest	PO/PSO
14	Parent-Teacher meet 2022	02-02-2022	32	Parent Relation Centre cell VCET, Puttur	-



The Department of Electronics and Communication Engineering in association with Parent Relation Centre cell organized Parent-Teacher meet on February 2<sup>nd</sup>, 2022. The meeting was conducted for the parents of the second-year students at 2:30 pm in Sri rama sabhabavana. Ms.Nisha G.R welcomed the gathering. Ms. Rajani Rai B, Department coordinator, PRC, briefed the objectives of the PRC cell, its functioning, and the importance of the Parent-Teacher meeting. Prof. Shrikanth Rao S K, HOD, EC Department addressed the parents and informed the rules and regulations of the college. He also brought to the notice of the parents about functioning of the department and delivery of academic tasks to the students. Ms.Lakshmi Bhat, PRC parent representative of 2<sup>nd</sup> year spoke to the audience about the role of the parents in the overall success of their children. The platform was open for discussion. Prof.Shrikanth Rao addressed the queries and assured that the department will render full support to enhance student learning. Ms.Prabha G.S. rendered vote of thanks. Ms.Disha S, PRC student coordinator was the master of the ceremony. 32 parents, class advisors and student counselors attended the meeting. Refreshment was arranged for all the parents. Following the session, the parents had discussion with the counsellors and faculty members in charge of the second-year classes.

**ACADEMIC YEAR 2021-2022**

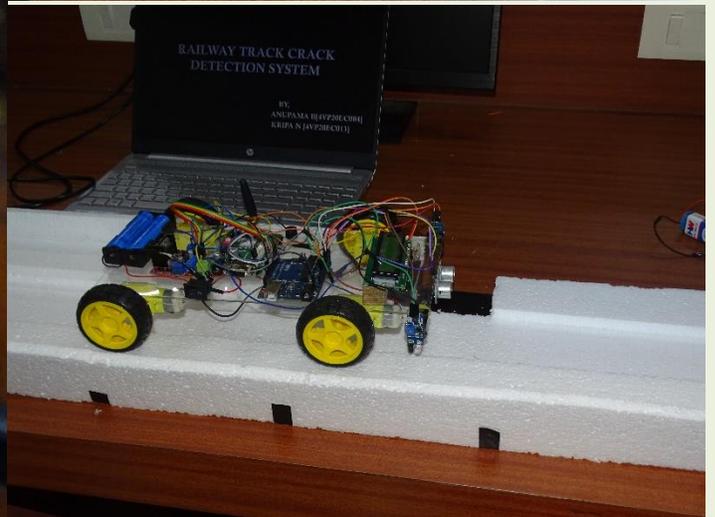
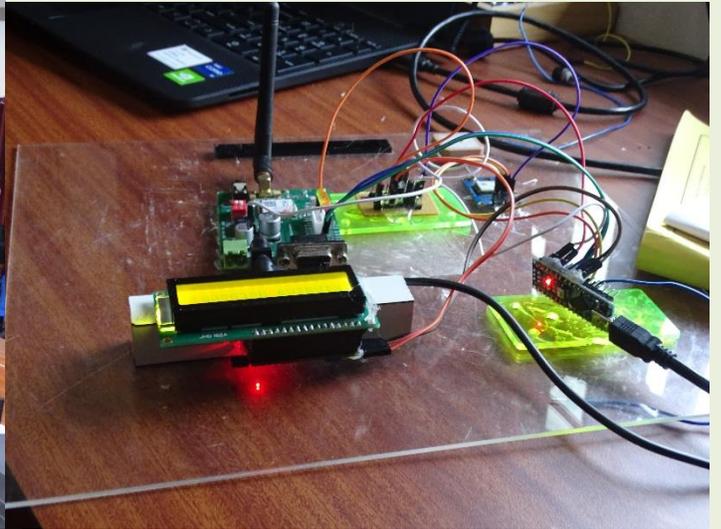
SNo	Activities	Date-Month- Year	Details of Participants	Details of Resource Person(s)/ Guest	PO/PSO
15	<b>Workshop on Virtual Labs</b>	<b>11-03-2022</b>	<b>2<sup>nd</sup> Year EC</b>	<b>Mrs. Vismaya Senior Research Fellow, NITK Surathkal. Mrs.Shreshta Senior Research Fellow NITK Surathkal.</b>	<b>PO1, PO2, PO3, PO4, PO12, PSO4</b>



The Department of Electronics and communication Engineering, Vivekananda College of Engineering, Puttur, organized a workshop on “Virtual Labs” on 11<sup>th</sup> March 2022, at 12 PM for second year EC students. The session was conducted by Mrs.Vismaya and Mrs. Shreshta working as Senior Research Fellow, NITK Surathkal. The session was conducted through MS Teams. There were 49 participants from Electronics and communication department of VCET. Prof. Akshay S P coordinated the session.

**ACADEMIC YEAR 2021-2022**

SNo	Activities	Date-Month- Year	Details of Participants	Details of Resource Person(s)/ Guest	PO/PSO
16	Mini Project Exhibition	11-03-2022	14 Teams of 2 <sup>nd</sup> Year EC	Department of ECE	PO1 PO2 PO3 PO5 PO12 PSO1



The Department of Electronics and Communication Engineering in association with AECE organized a Mini project exhibition on 11/03/2022 for 3<sup>rd</sup> semester students. Exhibited projects were related to the area of Embedded systems, IOT, Communication systems. Total 14 teams participated in the exhibition.

**ACADEMIC YEAR 2021-2022**

SNo	Activities	Date-Month- Year	Details of Participants	Details of Resource Person(s)/ Guest	PO/PSO
17	Three days' workshop on "Robotics and Embedded Systems"	27-05-2022 28-05-2022 30-05-2022	53	Mr. Dhananjai Sharma, Software engineer, Cisco systems Inc. San Jose, California, USA	PO1,PO2, PO3,PO4, PO5,PO6, PO9, PO11, PO12 PSO1



The Department of Electronics & Communication Engineering and Computer science engineering in association with ISTE and IEEE VCET student branch organized a three-day Workshop on “Robotics and Embedded system” held on 27, 28 and 30<sup>th</sup> May 2022. The workshop was conducted by resource person Mr. Dhananjai Sharma, Software engineer, Cisco systems Inc., San Jose, California, USA. Resource person gave hands on sessions to students with robotics kits. He ensures all of the participants get aware of the basics of using robotic kits, upload the programs into controller using different programming platforms. Students found interest and did more than 20 projects in these sessions. Total 53 students were participated in this session.

**ACADEMIC YEAR 2021-2022**

SNo	Activities	Date-Month- Year	Number of Participants	Details of Participants	PO/PSO
18	State level inter college techno cultural fest	27-05-2022	5	Akshath kumar, Vaibhav Bharadwaj, Vibhav Bharadwaj, Yashaswinarayana U A, Shravan salian	PO1 PO2 PO3 PO5 PO12 PSO1



Akshath kumar, Vaibhav Bharadwaj, Vibhav Bharadwaj, Yashaswi narayana U A, Shravan salian of 4<sup>th</sup>sem EC has won 1<sup>st</sup> prize in project expo (hardware) in Akriti 22 state level inter college techno-cultural fest which was held at Canara Engineering College, Mangalore on 27<sup>th</sup> May 2022 for the project title “IV bag monitoring system”.

**ACADEMIC YEAR 2021-2022**

SNo	Activities	Date-Month- Year	Number of Participants	Details of Participants	PO/PSO
19	Student Square	15-6-2022	2	Ms. Dhanya P and Ms. Praseeda	PO1 PO2 PO3 PO5 PO12 PSO1

**ST JOSEPH**  
 ENGINEERING COLLEGE  
 MANGALURU  
 AN AUTONOMOUS INSTITUTION


  
 S J E C STUDENT BRANCH

Department of  
 Electronics and Communication Engineering  
 and  
 IEEE SJEC Student Branch  
 presents

**STUDENT  
SQUARE**

**OPEN TO ALL B.E / B.Tech Students**
»»
**A Brainstorming Event On**
»»
**WIN CASH PRIZES Upto ₹2,500/-**

"Ideas and solutions to achieve any one of the UN Sustainable Development Goals"

**Venue:**  
 St Joseph Engineering College  
 Vamanjoor, Mangaluru.

**»» 15 June 2022  
10 am IST**


**JOIN US HERE**

[https://bit.ly/SJEC\\_SQR\\_22](https://bit.ly/SJEC_SQR_22)

Samyak Chandra  
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 (Student Co-ordinator)


  
 Ms Chaitra U R  
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 (Staff Co-ordinator)

Department of Electronics and Communication Engineering and IEEE SJEC student branch organized 'STUDENT SQUARE'- A Brainstorming event on 'IDEAS AND SOLUTIONS to achieve UN Sustainable Development Goals' held on 15<sup>th</sup> June 2022. Ms. Dhanya P and Ms. Praseeda of 4<sup>th</sup> sem EC secured 3<sup>rd</sup> place with topic Clean Water and Sanitation (SDG 6).

## ACADEMIC YEAR 2021-2022

SNo	Activities	Date-Month- Year	Number of Participants	Details of Participants	PO/PSO
20	IEEE Mangalore Sub Section -Student Scholarship	June 2022	1	Ms. Divyashree S	PO1 PO6 PO8 PO9 PO10



## IEEE BANGALORE SECTION STUDENT SCHOLARSHIP 2022 IEEE MANGALORE SUBSECTION



**Aniketh Shenoy K**  
Shri Madhwa Vadiraja Institute  
of Technology & MGMT,  
Bantakal



**Bharath HM**  
IEEE SB Canara  
Engineering College



**Divyashree S**  
VCET IEEE Student  
Chapter



**Janmansh Agarwal**  
IEEE National Institute of Technology  
Karnataka



**Rifah Sameen  
Sarang**  
PACE

# Congratulations!

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Ms. Divyashree S of 6<sup>th</sup>sem A section from department of ECE, received a scholarship based on the academics and the achievement from IEEE Bangalore section student scholarship 2022.

ACADEMIC YEAR 2021-2022

SNo	Activities	Date-Month- Year	Number of Participants	Details of Participants	PO/PSO
21	I2CONNECT-2022	18-06-2022	2	Ms. Jagrathi J Nayak Mr. Gautham Shankar	PO1 PO6 PO8 PO9 PO10

## IEEE Mangalore Subsection

EDC Block, First Floor

NMAM Institute of Technology, Nitte 574 110

Karkala Taluk, Udupi District



IEEE Mangalore Subsection

### Certificate of Merit

This is to certify that the project **Low Cost Hearing Aid** presented by **Gautham Shankar N, Jagrathi J Nayak** of **VIVEKANANDA COLLEGE OF ENGINEERING AND TECHNOLOGY, PUTTUR** under the guidance of **Prof Shivaprasad** has won **First** place in **SPS** track during **I2CONECCT 2022** (Industry Institute Conclave on Electronics, Computational and Communication Technologies) an Intercollegiate Project Exhibition and Competition held at National Institute of Technology Karnataka, Surathkal, India on June 18, 2022.

**Poornalatha G**  
Chair, IEEE MSS 2022

IEEE Mangalore Subsection has conducted I2CONNECT-2022 at NITK Surathkal on 18<sup>th</sup> June 2022. Under Signal Processing Section, there were 5 projects. Ms. Jagrathi J Nayak and Mr. Gautham Shankar N of 6<sup>th</sup> semester ECE of A section won the first prize for the project title "LOW-COST HEARING AID".