

**ACADEMIC  
YEAR 2018-2019**

SNo	Activities	Date-Month- Year	Details of Participants	Details of Resource Person(s)/ Guest	PO's
1	<b>AECE Inauguration</b>	<b>30 August 2018</b>	<b>Branch entry program for second year students</b>	<b>Sri. Ravikrishna Kallaje, Director, Management VCET</b>	<b>PO1, PO12</b>



Department of Electronics and Communication Engineering, Vivekananda College of Engineering and Technology, Puttur had organized branch entry program for second year students on August 30, 2018. Miss Rashmi and Miss Sindoorra Saraswathi of final year EC initiated the program with the prayer. Miss Sindoorra Saraswathi welcomed the dignitaries and gave the academic report of 2018-19. Prof. Shrikanth Rao, Head, Electronics and Communication department addressed the gathering. Chief guest of the function Sri. Ravikrishna D Kallaje addressed the students. President of the function, Dr. M S Govinde Gowda, Principal, VCET, gave the presidential address. Mr. Gurusandesh M, AECE coordinator, Mr. Sukesh Raj Kadambalithaya M R, Secretary AECE and all the teaching and non-teaching members of the Electronics and Communication department were present in the event. Mr. Sukesh Raj Kadambalithaya M R of final year EC, gave the vote of thanks. Miss. Harshitha K K and Miss. Pruthvi Rai of final year EC was the master of ceremony.

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2	<b>Talk on How to prepare for placements</b>	<b>14th November 2018</b>	<b>5th semester EC</b>	<b>Mr.Atul Krishna Engineer, Qualcomm, Singapore</b>	<b>PO8, PO9, PO10, PO11, PO12</b>



Department of Electronics and communication Engineering, Vivekananda College of Engineering and Technology, Puttur had organized interaction with Mr.Atul Krishna, proud Alumni of VCET-ECE. Mr.Atul Krishna Engineer, Qualcomm, Singapore. He clearly explained how to face interviews and expectations from the interviewer. He stressed on having a good communication skills, which will be important when introducing oneself in the interview or when it comes to answering to the question asked by the interviewer. The main objective of this session was to know about how to prepare for interviews, what is the latest technology, what needs to be done by the students to gear up to the expectations of the interviewer.

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3	How to prepare for placements	28 <sup>th</sup> August 2018	7th semester EC	Ms. Dhanyatha System engineer at Infosys , Chennai	PO8, PO9, PO10, PO11, PO12



Department of Electronics and communication Engineering, Vivekananda College of Engineering and Technology, Puttur had organized interaction with Miss Dhanyatha, proud Alumni of VCET-ECE, 2017 pass-out. She addressed students of final year ECE, on 28<sup>th</sup> August 2018. Miss Dhanyatha is a System Engineer at Infosys Technologies Ltd located at Chennai. She clearly explained how to face interviews and expectations from the interviewer. She stressed on having a good communication skills, which will be important when introducing oneself in the interview or when it comes to answering to the question asked by the interviewer. She gave a brief view of the interview process and how students have to answer to the question asked.

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4	AECE Valedictory and Farewell to final year students	26 <sup>th</sup> May 2018	EC students and faculty	Sri. Ravikrishna Kallaje, Director, Management VCET	PO1, PO12



On May 26, 2018, department of EC conducted valedictory function for AECE and farewell to final year Electronics and Communications students. Program started with a prayer by Miss Preethi, 6<sup>th</sup> sem EC. Miss Harshitha K K, 6<sup>th</sup> sem EC gave the welcome speech. Miss Sindoorra Saraswathi, Secretary AECE, gave the AECE report. Prof. Srikanth Rao S K, HOD, EC addressed the students. Chief guest of the function, Sri. Ravikrishna D Kallaje addressed the students. He gave away the prizes for Circuit gallery winners and for the best final year project. Department Magazine Dhvani was released by Chief Guest of the function Sri. Ravikrishna D Kallaje. Few final year students and some faculty shared their experiences. President of the function, Dr. M S Govinde Gowda, Principal, VCET, gave the presidential address.

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5	Inauguration of Basic Electrical Engineering Lab	10 <sup>th</sup> October 2018	DUGC members, EC staffs and HODs	Sri. Ravikrishna D Kallaje addressed the students	PO1, PO2, PO3, PO4, PO12, PSO1



Department of Electronics and communication Engineering, Vivekananda College of Engineering and Technology, Puttur had organized a program to inaugurate the Basic electrical engineering lab. Lab provides students with hands-on experience and help them to work in lab on different projects to implement their idea. Lab runs in both the semester and accommodates about 150 students per semester. The laboratory has electrical experiments components to study basic laws, three phase concepts, motor working and fault concepts.

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6	Faculty development program on "Fundamentals of Electromagnetics and Wave Propagation"	26 <sup>th</sup> to 28 <sup>th</sup> July 2018	All EC department faculties.	1) Dr.U.Shripathi Acharya, Professor, Department of EC,NITK Surathkal  2) Mr. Raghavendra, ResearchScholar, Dept of ECE, NITK, Surathkal	PO1,PO2,P O12,PSO2



Department of EC, VCET Puttur in association with ISTE had organized the FDP on "Fundamentals of Electromagnetics and Wave Propagation" for faculties of EC dept on 26<sup>th</sup> to 28<sup>th</sup> July 2018. Dr.U.Shripathi Acharya, Prof, Department of EC,NITK Surathkal. Mr. Raghavendra, Research Scholar, Dept of ECE, NITK, Surathkal were the resource persons. Principal Dr. M.S Govinde Gowda, President Sri K. Vishwas Shenoy, Chief Guest Dr.Abdul Sharief, Guest of Honour Sri. Ravikrishna D.Kallaje and HoD Sri.Shrikanth Rao. were present on the stage. The Chief Guest Dr.Abdul Sharief inaugurated the programme by lightening the lamp and explained the significance of FDP briefly.

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7	<b>Interactive Session on “Sensors used in Industry”</b>	23 <sup>rd</sup> August 2018	<b>Pre-final year EC, CS and Mechanical students.</b>	<b>Mr.Kishan Bhat and Mr.Venugopal, Servocontrols Aerospace India pvt ltd,Belagavi.</b>	<b>PO1, PO2,PO3, PO4,PO5, PO10, PO12</b>



On August 23, 2018 Association of Electronics and communication Engineering conducted an interactive session on sensors used in industry for 5<sup>th</sup> sem EC, CS and Mechanical students. Mr. Kishan Bhat took a session on electronic sensors used in Mechanical industry, mainly in satellite launching vehicles. Mr. Venugopal explained about different types of satellites and the structure of Nano satellite to the students.

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8	<b>Inauguration of Industry Connect IoT lab</b>	<b>24th September 2018</b>	<b>All HOD's Industry Connect IoT lab team members, and 1<sup>st</sup> year students.</b>	<b>Sri.V.V.Bhat Rtd. Senior IAS officer.</b>	<b>PO1, PO2, PO3, PO8, PO9, PO11, PO12</b>



Vivekananda College of Engineering and Technology, Puttur organized inauguration of “Industry connect IoT lab” on September 24, 2018. Function was inaugurated by lighting the lamp by Dr.Kalladka Prabhakar Bhat, President, Vivekananda Vidyavardhaka Sangha, Puttur. On this occasion he told having a Industry connect IoT lab will facilitate continuous learning in technical institution and also best wishes were conveyed. Prof.Gurusandesh M, Coordinator, Industry Connect IoT lab, VCET, Puttur. welcomed the dignitaries. Sri.Muralidhar Bhat, Treasurer, Vivekananda College of Engineering & Technology governing council, Puttur. addressed the gathering. He told that innovations are a part of our daily life activities, through this lab innovative projects will be coming out in future. Chief Guest: Sri.V.V.Bhat ,Rtd.Senior IAS, officer addressed the gathering.



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9	Workshop on Embedded Electronics and IoT Applications	13 <sup>th</sup> and 14 <sup>th</sup> of october 2018	5 <sup>th</sup> sem EC students	Mr. Mohammed Sadiq, Director, Tech-Graylogix, Mangalore and Mr. Mudassar Pasha, Tech-Graylogix, Mangalore	PO1,PO2,PO3, PO4,PO5, PO8, PO9, PO12  PSO1



Two day Workshop on Embedded Electronics and IoT Applications was organized by Department of Electronics and Communication Engineering in association with Association of Electronics and Communication Engineering on 13<sup>th</sup> and 14<sup>th</sup> of october 2018. Mr. Mohammed Sadiq, Director, Tech-Graylogix, Mangalore and Mr. Mudassar Pasha, Tech-Graylogix, Mangalore were the resource persons.41 students from 5<sup>th</sup> sem Electronics and Communication Engineering attended the workshop.

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10	To train the students to meet the expectations of the industry	5th September 2018	7th semester EC	Mrs Kavyanjali H M	PO8, PO9, PO10, PO11, PO12



Department of Electronics and communication Engineering, Vivekananda College of Engineering and Technology, Puttur had organized interaction with Mrs Kavyanjali H M, proud Alumni of VCET-ECE, 2013 pass-out. She addressed students of final year ECE, on 5<sup>th</sup> September 2018. Mrs Kavyanjali is a System Engineer in Wipro, Bangalore. She told them to pick up one programming language, and make efforts to master it. She clearly explained how to face interviews and expectations from the interviewer from non IT branch students. She discussed few C programs that are usually asked in the interview. Students were informed about the important topics they have to know in C and C++ before going for interview. The main objective of this session was to know about how to prepare for interviews, what is the latest technology, what needs to be done by the students to gear up to the expectations of the interviewer.

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11	Workshop on Basic Electrical & Electronics to Kombettu College Students	31 <sup>st</sup> Oct 2018	II PUC Students	Prof. Sowmya Anil , Prof.Prabha G S	PO1, PO2, PO3, PO4, PO12, PSO1



Department of Electronics & Communication, Vivekananda College of Engineering and Technology, Puttur conducted workshop on basic Electronics & Electrical on October 31<sup>st</sup>, 2018 for II PUC students of Kombettu College. Prof. Sowmya Anil, Assistant Professor, Electronics & Communication, Vivekananda College of Engineering and Technology, Puttur gave a presentation on basic working of Circuit mini breaker, different components, short and open circuit concepts, fuse, power consumed by different lamps. Prof. Prabha G S, Assistant Professor, Electronics & Communication, Vivekananda College of Engineering and Technology, Puttur explained about working of function generator, DC power supply, CRO, soldering, active and passive components.

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12	<b>‘VISHWA VIJETHA DAY’</b>	<b>11<sup>th</sup> Sept,2018.</b>	<b>Faculty, staff and students</b>	<b>Sri. Mahesh Pucheppady, Chief Incharge, Hosadigantha,</b>	<b>PO12</b>



The “125<sup>th</sup> Anniversary of Illustrious Speech at Parliament of Religions, Chicago on 11<sup>th</sup> Sept, 1893 by Swami Vivekananda” was celebrated as ‘VISHWA VIJETHA DAY’ by ‘Viveka Sankalpa’, Vivekananda College of Engg & Tech, Puttur – 574203 on 11<sup>th</sup> Sept, 2018. Function started with an invocation by Ms. Rashmi and Ms. Prithvi C. Rai, 7<sup>th</sup> Sem E&C. Ms. Sindura Saraswathi, 7<sup>th</sup> Sem E&C welcomed the Chief Guest and the dignitaries. The Chief Guest Sri. Mahesh Pucheppady, Chief Incharge, Hosadigantha, Puttur lightened the lamp and talked about Swami Vivekananda and his Chicago speech. Presidential remark was given by Sri. Sathish Rao. P, President, VCET governing council, Puttur. Sri. Radhakrishna Bhaktha, Dr. M.S Govinde Gowda, Principal, VCET Puttur presided over the function. Almost 60 participants including faculty, staff and students participated in the event with great enthusiasm. The entire function lasted for an hour. Essay competition was conducted on account of this on the topic “Vivekananda and modern India” and Speech on the topic Vivekananda as a youth icon. The winners were awarded with prizes. Mr. Udaya Kumar S, Associate Professor, Dept of ECE, VCET, Puttur convened the program.

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13	Placement activity/Alumnitalk on "Placement Readiness"	11 <sup>th</sup> August2018	Final year EC students	1)Ms. Ahana Rai, HR. learning and Training, Seventh Sense solutions 2)Ms. Jesna Menezes, System engineer, Infosys	PO8, PO9, PO10, PO11, PO12



A Placement activity was conducted by the department of EC to help our final year students for the placement drives coming up. Campus placements is not viable until and unless students are industry ready. The speaker for the 1<sup>st</sup> session was Ms Ahana Rai, HR. learning and Training, Seventh Sense solutions. She spoke about the importance of having a good language and confidence during the interview. Ms Jesna M, System engineer, Infosys was the speaker for the second session. She gave an insight into typical Campus Recruitment Levels that every Company follows and how students have to prepare and plan to face those levels.

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14	<b>Bridging gaps between company expectations &amp; academic learning.</b>	<b>14<sup>th</sup> September 2018</b>	<b>Final year EC students</b>	<b>Mr.Pramod Baliga Senior Design Engineer at AMD, Bangalore</b>	<b>PO1,PO8, PO9, PO10, PO11, PO12</b>



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15	Carrier guidance to students.	15th October 2018	Pre final EC	Mr.Sujan Raj Automation Engineer & Mr.Ramesh S C Embedded Engineer	PO8, PO9, PO10, PO11, PO12



Department of Electronics and communication Engineering, Vivekananda College of Engineering and Technology, Puttur had organized interaction with Mr.Ramesh S C , proud Alumni of VCET-ECE, 2016 pass-out. He addressed students of pre - final year ECE, on 15<sup>th</sup> October 2018. Ramesh S C is also a co-founder of Slekin Technical Solutions Pvt Ltd. They both gave a talk on Industrial Automation and embedded systems also about carrier guidance to pre- final year students. The main objective of this session was to know about latest technology, and about entrepreneurship. Prof. Shrikanth Rao, HOD ECE, Prof.Subrahmanya Bhat K G, Department of ECE, Vivekananda College of Engineering and Technology, were also present in the discussion.

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16	Technical talk on drones.	8 <sup>th</sup> April 2019	3 <sup>rd</sup> year EC students.	Mr. Prathap N M, third year Bsc, JSS college of Arts, Commerce and Science, Mysore (recognized as young scientist by government of India)	PO1,PO2,P08, PO9, PO10,PO11 , PO12



A technical talk on Drones was organized by Department of Electronics and Communication Engineering in association with Association of Electronics and Communication Engineering on 8<sup>th</sup> April 2019 at 3:00 PM, Sri Rama Sabha Bhavana, VCET Puttur. Mr. Prathap N M took a session on the history of the development of Drones and the different stages in the development of Drones. He also explained our students about the importance and the applications of Drones in different areas. 30 students from 6<sup>th</sup> sem Electronics and Communication Engineering attended the workshop.



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17	Workshop on Arduino programming	15 <sup>th</sup> March 2019	Vivekananda P Ucollege and St.Philomena PU College	Department of ECE, VCETPuttur.	PO1, PO2, PO3, PO8, PO9, PO11, PO12



A one-day Workshop on Arduino programming was organized by Department of Electronics and Communication Engineering in association with Arduino club on 15<sup>th</sup> of March 2019 at Industry Connect IOT lab. Interested 20 first puc science students from Vivekananda P U college and St.Philomena PU College made use of this workshop. Hands on training was given to these students on using Arduino kit. Coordinator of Arduino club Mr.Naveena C and members Mr.Shivaprasad , Mr.Gurusandesh , Ms.Jovita Lasrado, Mr.Subrahmanya Bhat KG , Mr.Manoj K P, Mr.Mahabaleshwara Bhat and Mr.Yadunandana were present during the workshop.

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18	IEEE VCET STUDENT BRANCH	30 <sup>th</sup> March 2019	Students from different branches	Dr. Manohar Pai ,chair of IEEE Mangalore sub-section	PO1,P012



Behalf of creating the IEEE student branch in Vivekananda college of engineering and technology Puttur, the inaugural function of IEEE student was held on 30/03/2019. On this occasion a technical talk on “**Research leading to entrepreneurship**” was held by **Dr. Manohar Pai**, chair of IEEE Mangalore sub-section from 11.00 AM to 1.00 PM. The inauguration of the IEEE VCET STUDENT BRANCH started from 2.00PM to 3.00 PM. IEEE VCET student branch was inaugurated by lighting the lamp and unveiling the branch logo. Chief guest Dr. Manohar Pai, chair IEEE mangalore sub-section encouraged the students with his warm and motivational words. President of the function Sri Ravikrishna D. Kallaje praised and motivated members. VCET branch counsellor Dr. Raghavendra S. spoke about the initiation of student branch and participation of students. Student branch chair Mr. Sooryanarayana presented the event list.

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19	Circuit Gallery	13 <sup>th</sup> May 2019	6 <sup>th</sup> sem and 4 <sup>th</sup> sem EC students	Dr. M. S. Govinde gowda(Principal) Prof. Shrikanth Rao S K(HOD,Dept of EC)	PO1, PO2, PO3, PO8, PO9, PO11, PO12



Department of EC, VCET Puttur had organized competition “Circuit Gallery-2019 project Exhibition” for 6<sup>th</sup> EC and 4<sup>th</sup> EC on 13<sup>th</sup> May 2019. Dr. M. S. Govinde gowda, Principal was the chief guest. Prof. Shrikanth Rao S K(HOD EC) was also present on the dias. The main objective of this session was to provide platform to exhibit their technical skills. This helps the students to learn technology and develop some of the fine skills such as teamwork, communication skills, solving complex problems, expressing the concepts and conceptualizing any problem and its solution. Principal Dr. M. S. Govinde gowda told that these kinds of activities provides opportunity to students to develop practical skills. Ms. Harshitha K K , Final year EC student rendered master of ceremony.